



FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.
1875.1280001APPLICATION NO.
10/085,071APPLICANT
Gopinathan et al.FILING DATE
March 1, 2002GROUP
2662 2665

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
DH	AA1	5,485,490	01/1996	Leung et al.			
DH	AB1	5,650,954	07/1997	Minuhin			
DH	AC1	5,757,857	05/1998	Buchwald			
DH	AD1	5,881,108	03/1999	Herzberg et al.			
DH	AE1	5,996,415	12/1999	Stanke et al.			
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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
DH	AL1	0 483 439 A1	05/1992	EPO	H04L	25/03	Yes No
DH	AM1	0 805 447 A2	11/1997	EPO	G11B	20/10	Yes No
DH	AN1	1 139 619 A1	10/2001	EPO	H04L	25/02	Yes No
DH	AO1	WO 01/29991 A2	04/2001	PCT	H04B	10/00	Yes No
DH	AP1	WO 01/54317 A2	07/2001	PCT	H04B	10/00	Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DH	AR	1	Agazzi, O. and Lenosky, T., "Algorithm to Postprocess Measured Data," IEEE 802.3ae Equalization Ad Hoc Group, IEEE, January 10, 2001, 11 pages.
DH	AS	1	Agazzi, O., "A Link Model for Equalized Optical Receivers," IEEE 802.3ae Equalization Ad Hoc Group, IEEE, March 11, 2001, 30 pages.
DH	AT	1	Agazzi, O. et al., "DSP-Based Equalization for Optical Channels: Feasibility of a VLSI Implementation," IEEE 802.3ae, New Orleans, IEEE, September 12-14, 2000, 39 pages.

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
DH	AL2	WO 01/65788 A2	09/2001	PCT	H04L	25/00	Yes No
DH	AM2	WO 02/13424 A2	02/2002	PCT	H04B	10/158	Yes No
	AN2						Yes No
	AO2						Yes No
	AP2						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DH	AR	2	Agazzi, O. et al., "Interim Observations on Multimode Optical Channels," IEEE 802.3ae - Equalization Ad Hoc, IEEE, Tampa, November 5, 2000, 29 pages.
DH	AS	2	Agazzi, O. et al., "Measurements of DMD-Challenged Fibers at 3.125Gb/s," IEEE 802.3ae Equalization Ad Hoc, IEEE, January 10, 2001, 33 pages.
DH	AT	2	Agazzi, O. and Lenosky, T., "Measurement of Non-Stationarity of 10 Gb/s Multimode Fiber Links," IEEE 802.3ae Equalization Ad Hoc, IEEE, November 24, 2000, 5 pages.

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	AN3						Yes No
	AO3						Yes No
	AP3						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DM	AR	3	Agazzi, O., "10 Gb/s PMD Using PAM-5 Modulation," IEEE 802.3, IEEE, Dallas, January 18-20, 2000, 19 pages.
DM	AS	3	Agazzi, O. et al., "10 Gb/s PMD Using PAM-5 Trellis Coded Modulation," IEEE 802.3, IEEE, Albuquerque, March 6-10, 2000, 38 pages.
DM	AT	3	Bhatt, V., "Equalization Ad Hoc Concluding Report," IEEE P802.3ae Plenary, IEEE, March 2001, 12 pages.

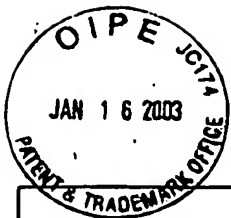
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	AN4						Yes No
	AO4						Yes No
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OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DH	AR	4	Bingham, J.A.C., "Multicarrier Modulation for Data Transmission: An Idea Whose Time Has Come," <i>IEEE Communications Magazine</i> , IEEE, Vol. 28, No. 5, pages 5-8 and 11-14.
DH	AS	4	Chiddix, J. et al., "AM Video on Fiber in CATV Systems: Need and Implementation," <i>IEEE Journal on Selected Areas in Communications</i> , IEEE, Vol. 8, No. 7, September 1990, pages 1229-1239.
DH	AT	4	Darcie, T., "Subcarrier Multiplexing for Lightwave Networks and Video Distribution Systems," <i>IEEE Journal on Selected Areas in Communications</i> , IEEE, Vol. 8, No. 7, September 1990, pages 1240-1248.

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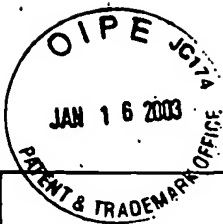
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	AM5						Yes No
	AN5						Yes No
	AO5						Yes No
	AP5						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DH	AR	5	Fettweis, G., "High-Rate Viterbi Processor: A Systolic Array Solution," <i>IEEE Journal on Selected Areas in Communications</i> , IEEE, Vol. 8, No. 8, October 1990, pages 1520-1534.
DH	AS	5	Fettweis, G., "Parallel Viterbi Algorithm Implementation: Breaking the ACS-Bottleneck," <i>IEEE Transactions on Communications</i> , IEEE, Vol. 37, No. 8, August 1989, pages 785-790.
DH	AT	5	Fornev, Jr., G.D., "The Viterbi Algorithm," <i>Proceedings of the IEEE</i> , IEEE, Vol 61, No. 3, March 1973, pages 268-278.

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	AL6						Yes No
	AM6						Yes No
	AN6						Yes No
	AO6						Yes No
	AP6						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DH	AR	6	Frazier, H., "IEEE 802.3 Higher Speed Study Group," 10 Gig MII update, IEEE, Kauai, Hawaii, November 9, 1999, 24 pages.
DH	AS	6	Giaretta, G. and Lenosky, T., "Adaptive Equalization of DMD Challenged Multimode Fiber at 1300 nm," IEEE P802.3ae Plenary, IEEE, March 11, 2001, 10 pages.
DH	AT	6	Hatamian, M. et al., "Design Considerations for Gigabit Ethernet 1000Base-T Twisted Pair Transceivers," IEEE 1998 Custom Integrated Circuits Conference, IEEE, 1998, pages 335-342.

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
	AL7						Yes No
	AM7						Yes No
	AN7						Yes No
	AO7						Yes No
	AP7						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DH	AR	2	Isaacs, M et al., "Measurements of Fiber Responses at 5 Gb/s Data Rate Using 850nm VCSELs," <i>IEEE 802.3ae Equalization Ad Hoc</i> , IEEE, March 11, 2001, 18 pages.
DH	AS	2	Kanno, N. and Ito, K., "Fiber-Optic Subcarrier Multiplexing Video Transport Employing Multilevel QAM," <i>IEEE Journal on Selected Areas in Communications</i> , IEEE, Vol. 8, No. 7, September 1990, pages 1313-1319.
DH	AT	2	Kasper, B.L., "Equalization of Multimode Optical Fiber Systems," <i>The Bell System Technical Journal</i> , American Telephone and Telegraph Company, IEEE, Vol. 61, No. 7, September 1982, pages 1367-1388.

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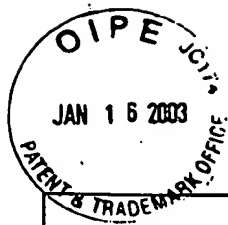
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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL8						Yes No
	AM8						Yes No
	AN8						Yes No
	AO8						Yes No
	AP8						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)			
DH	AR	8	Kasturia, S. and Winters, J., "Techniques for High-Speed Implementation of Nonlinear Cancellation," <i>IEEE Journal on Selected Areas in Communications</i> , IEEE, Vol. 9, No. 5, June 1991, pages 711-717.
DH	AS	8	Lenosky, T., "A Unified Method of Calculating PMD-induced Pulse Broadening," <i>IEEE 802.3ae Equalization Ad Hoc Meeting</i> , IEEE, Tampa, Florida, November 5, 2000, 8 pages.
DH	AT	8	Lenosky, T. and Giaretta, G., "Five Gb/s Multimode DMD at 850 nm: Real-Time Data and Equalizer Simulations," <i>Finisar Corporation</i> , March 11, 2001, 13 pages.

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	AL9						Yes No
	AM9						Yes No
	AN9						Yes No
	AO9						Yes No
	AP9						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DH	AR	9	Lenosky, T. et al., "Measurements of DMD-Challenged Fibers at 850nm and 2Gb/s Data Rate," <i>IEEE 802.3ae - Equalization Ad Hoc Group</i> , IEEE, January 10, 2001, 21 pages.
DH	AS	9	Lenosky, T. et al., "Measurements of DMD-Challenged Fibers at 1310nm and 1Gb/s Data Rate," <i>IEEE 802.3ae - Equalization Ad Hoc Group</i> , IEEE, January 10, 2001, 21 pages.
DH	AT	9	Liu, M-K. and Modestou, P., "Multilevel Signaling and Pulse Shaping for Spectrum Efficiency in Subcarrier Multiplexing Transmission," <i>IEEE Journal of Lightwave Technology</i> , IEEE, Vol. 12, No. 7, pages 1239-1246.

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	AL10						Yes No
	AM10						Yes No
	AN10						Yes No
	AO10						Yes No
	AP10						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DH	AR	10	Olshansky, R. et al., "Subcarrier Multiplexed Coherent Lightwave Systems for Video Distribution," <i>IEEE Journal on Selected Areas in Communications</i> , IEEE, Vol. 8, No. 7, September 1990, pages 1268-1275.
DH	AS	10	Olshansky, R. et al., "Subcarrier Multiplexed Lightwave Systems for Broadband Distribution," <i>IEEE Journal of Lightwave Technology</i> , IEEE, Vol. 7, No. 9, September 1989, pages 1329-1341.
DH	AT	10	Otte, S. and Rosenkranz, W., "A Decision Feedback Equalizer for Dispersion Compensation in High Speed Optical Transmission Systems," <i>International Conference on Transparent Optical Networks</i> , IEEE, 1999, pages 19-22.

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	AL11						Yes No
	AM11						Yes No
	AN11						Yes No
	AO11						Yes No
	AP11						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DM	AR	11	Parhi, K. et al., "Parallel Implementation of the DSP Functions of the PAM-5 10Gb/s Transceiver," IEEE 802.3ae Task Force, IEEE, March 2000, 12 pages.
DM	AS	11	Peral, E. et al., "Measurements of time variation in DMD-challenged multimode fiber at 1310nm for 10GE equalizer applications.," IEEE P802.3ae Equalization Ad Hoc, IEEE, March 2001, 19 pages.
DM	AT	11	Personick, S.D., "Baseband Linearity and Equalization in Fiber Optic Digital Communication Systems," Bell System Technical Journal, American Telephone and Telegraph Company, Vol. 52, No. 7, September 1973, pages 1175-1194.

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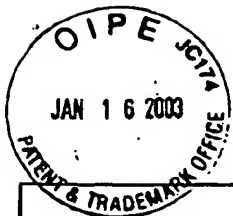
DH	AR	12	Personick, S.D., "Receiver Design for Digital Fiber Optic Communication Systems, I," <i>Bell System Technical Journal</i> , American Telephone and Telegraph Company, Vol. 52, No. 6, July-August 1973, pages 843-874.
DH	AS	12	Personick, S.D., "Receiver Design for Digital Optic Systems," <i>National Telecommunications Conference</i> , IEEE, Atlanta, Georgia, November 26-28, 1973, Vol. 1, pages 8E-1 - 8E-4.
DH	AT	12	Vorenkamp, P. et al., "Analog Interface for 10-Gb/s Ethernet," <i>IEEE 802.3ae Task Force</i> , IEEE, March 2000, 13 pages.

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OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DM	AR	13	Winters, J. and Gitlin, R., "Electrical Signal Processing Techniques in Long-Haul Fiber-Optic Systems," <i>IEEE Transactions on Communications</i> , IEEE, Vol. 38, No. 9, September 1990, pages 1439-1453.
DM	AS	13	Winters, J. et al., "Reducing the Effects of Transmission Impairments in Digital Fiber Optic Systems," <i>IEEE Communications Magazine</i> , IEEE, June 1993, pages 68-76.
DM	AT	13	"Progress Report on Equalization of Multimode Fibers," <i>Ad Hoc Group on Equalization</i> , IEEE, January 12, 2001, 16 pages.

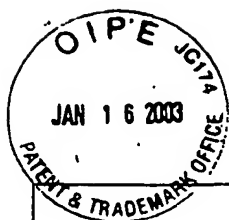
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	AL14						Yes No
	AM14						Yes No
	AN14						Yes No
	AO14						Yes No
	AP14						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DH	AR	14	Alderrou, D. et al., "XAUI/XGXS Proposal," IEEE 802.3ae Task Force, IEEE, May 23-25, 2000, 28 pages.
DH	AS	14	Winters, J.H. and Gitlin, R., "Electrical Signal Processing Techniques in Long-Haul, Fiber-Optic Systems," IEEE International Conference on Communications, IEEE, Vol. 1, April 16-19, 1990, pages 0397-0403.
	AT	14	

EXAMINER

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DATE CONSIDERED 12-20-05

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.



FORM PTO-1449		ATTY. DOCKET NO. 1875.1280001		APPLICATION NO. 10/085,071			
THIRD SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT		INVENTORS AGAZZI <i>et al.</i>		ART UNIT 2665			
FILING DATE March 1, 2002							
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
DH	AA1	5,396,224	03/1995	Dukes <i>et al.</i>	—	—	
DH	AB1	5,550,546	08/1996	Noneman <i>et al.</i>	—	—	
DH	AC1	5,554,945	09/1996	Lee <i>et al.</i>	—	—	
DH	AD1	5,614,855	03/1997	Lee <i>et al.</i>	—	—	
DH	AE1	5,703,905	12/1997	Langberg	—	—	
DH	AF1	5,768,268	06/1998	Kline <i>et al.</i>	—	—	
DH	AG1	5,822,143	10/1998	Cloke <i>et al.</i>	—	—	
DH	AH1	5,881,107	03/1999	Termerinac <i>et al.</i>	—	—	
DH	AI1	5,945,862	08/1999	Donnelly <i>et al.</i>	—	—	
DH	AJ1	5,949,820	09/1999	Shih <i>et al.</i>	—	—	
DH	AK1	5,966,415	10/1999	Bliss <i>et al.</i>	—	—	
FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
DH	AL1	EP 0 515 074	11/1992	EP	—	—	N/A
DH	AM1	EP 0 909 035	04/1999	EP	—	—	N/A
DH	AN1	WO 01/84702	11/2001	WO	—	—	N/A
DH	AO1	WO 01/84724	11/2001	WO	—	—	N/A
DH	AP1	WO 02/071616	09/2002	WO	—	—	N/A
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
DH	AR1	Buchwald, A. and Martin, K., <i>Integrated Fiber-Optic Receivers</i> , Kluwer Academic Publishers, ISBN 0-7923-9549-2, Copyright 1995 (entire book submitted).					
DH	AS1	Sidiropoulos, S. and Horowitz, M. A., "A Semidigital Dual Delay-Locked Loop," <i>IEEE Journal of Solid-State Circuits</i> , IEEE, Vol. 32, No. 11, November 1997, pp. 1683-1692.					
	AT1						
EXAMINER				DATE CONSIDERED			
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THIRD SUPPLEMENTAL
INFORMATION DISCLOSURE STATEMENTPAT. DOCKET NO.
1875.1280001APPLICATION NO.
10/085,071INVENTORS
AGAZZI *et al.*FILING DATE
March 1, 2002ART UNIT
2665

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
DH	AA2	6,002,279	12/1999	Evans <i>et al.</i>	—	—	
DH	AB2	6,005,445	12/1999	Katakura	—	—	
DH	AC2	6,009,534	12/1999	Chiu <i>et al.</i>	—	—	
DH	AD2	6,038,269	03/2000	Raghavan	—	—	
DH	AE2	6,122,336	09/2000	Anderson	—	—	
DH	AF2	6,134,268	10/2000	McCoy	—	—	
DH	AG2	6,329,859 B1	12/2001	Wu	—	—	
DH	AH2	6,359,486 B1	03/2002	Chen	—	—	
DH	AI2	6,397,048 B1	05/2002	Toda	—	—	
DH	AJ2	6,404,525 B1	06/2002	Shimomoura <i>et al.</i>	—	—	
DH	AK2	6,498,694 B1	12/2002	Shah	—	—	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL2						Yes No
	AM2						Yes No
	AN2						Yes No
	AO2						Yes No
	AP2						Yes No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	AR2	
	AS2	
	AT2	

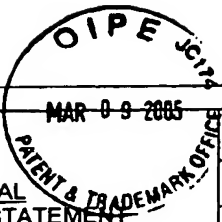
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FORM PTO-1449

MAR 09 2005

ATTY. DOCKET NO.

1875.1280001

APPLICATION NO.

10/085,071

THIRD SUPPLEMENTAL
INFORMATION DISCLOSURE STATEMENT

INVENTORS

AGAZZI *et al.*

FILING DATE

March 1, 2002

ART UNIT

2665

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
DH	AA3	6,509,773 B2	01/2003	Buchwald <i>et al.</i>	1	1	
DH	AB3	6,621,862 B1	09/2003	Dabell	1	1	
DH	AC3	6,791,388 B2	09/2004	Buchwald <i>et al.</i>	1	1	
DH	AD3	2002/0012152 A1	01/2002	Agazzi <i>et al.</i>	1	1	
DH	AE3	2002/0034222 A1	03/2002	Buchwald <i>et al.</i>	1	1	
DH	AF3	2002/0039395 A1	04/2002	Buchwald <i>et al.</i>	1	1	
DH	AG3	2002/0044617 A1	04/2002	Buchwald <i>et al.</i>	1	1	
DH	AH3	2002/0044618 A1	04/2002	Buchwald <i>et al.</i>	1	1	
DH	AI3	2003/0086515 A1	05/2003	Trans <i>et al.</i>	1	1	
DH	AJ3	2004/0212416 A1	10/2004	Buchwald <i>et al.</i>	1	1	
	AK3						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
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	AM3						Yes No
	AN3						Yes No
	AO3						Yes No
	AP3						Yes No

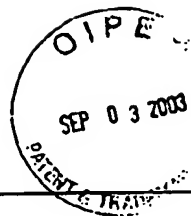
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

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DATE CONSIDERED

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FORM PTO-1449 <u>SECOND SUPPLEMENTAL INFORMATION</u> <u>DISCLOSURE STATEMENT</u>	ATTY. DOCKET NO. 1875.1280001	APPLICATION NO. 10/085,071
	APPLICANT Gopinathan et al.	
	FILING DATE March 1, 2002	GROUP 2665

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA1						
	AB1						
	AC1						
	AD1						
	AE1						
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	AH1						
	AI1						
	AJ1						
	AK1						

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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
DA	AL1	EP 1 006 697 A2	06/2000	EP			N/A
	AM1						
	AN1						
	AO1						
	AP1						

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DA	AR	1	Copy of International Search Report for PCT Application Number PCT/US02/06019, filed March 1, 2002, 7 pages (mailing date of search report - August 1, 2003).
	AS	1	
	AT	1	

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FORM PTO-1449

**FIRST SUPPLEMENTAL INFORMATION
DISCLOSURE STATEMENT**ATTY. DOCKET NO.
1875.1280001APPLICATION NO.
10/085,071APPLICANT
Gopinathan et al.FILING DATE
March 1, 2002GROUP
2662 2665**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA1						
	AB1						
	AC1						
	AD1						
	AE1						
	AF1						
	AG1						
	AH1						
	AI1						
	AJ1						
	AK1						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
	AL1						Yes No
	AM1						Yes No
	AN1						Yes No
	AO1						Yes No
	AP1						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DA	AR	1	Black, Jr., W. and Hodges, D., "Time Interleaved Converter Arrays," <i>IEEE Journal of Solid-State Circuits</i> , IEEE, Vol. SC-15, No. 6, December 1980, pages 1022-1029.
DH	AS	1	Conroy, C. et al., "An 8-b 85-MS/s Parallel Pipeline A/D Converter in 1- μ m CMOS," <i>IEEE Journal of Solid-State Circuits</i> , IEEE, Vol. 28, No. 4, April 1993, pages 447-454.
DH	AT	1	Dally, W. and Poulton, J., "Transmitter Equalization for 4Gb/s Signalling," <i>Proceedings of Hot Interconnects IV</i> , Palo Alto, CA, 1996, 10 pages.

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	APPLICANT Gopinathan et al.	
	FILING DATE March 1, 2002	GROUP 2662 2665

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA2						
	AB2						
	AC2						
	AD2						
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
	AL2						Yes No
	AM2						Yes No
	AN2						Yes No
	AO2						Yes No
	AP2						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DH	AR	2	Ellersick, W. et al., "A Serial-Link Transceiver Based on 8GSample/s A/D and D/A Converters in 0.25µm CMOS," <i>IEEE International Solid-State Circuits Conference</i> , IEEE, 2001, page 58-59 and 430.
DH	AS	2	Ellersick, W. et al., "GAD: A 12-GS/s CMOS 4-bit A/D Converter for an Equalized Multi-Level Link, <i>Symposium on VLSI Circuits Digest of Technical Papers</i> , 1999, pages 49-52.
DH	AT	2	Eklund, J-E. and Gustafsson, F., "Digital Offset Compensation of Time-Interleaved ADC Using Random Chopper Sampling," <i>IEEE International Symposium on Circuits and Systems</i> , IEEE, May 28-31, 2000, pages II-447-450.

EXAMINER DUC H0	DATE CONSIDERED 12-20-05
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	APPLICANT Gopinathan et al.	
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA3						
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	AI3						
	AJ3						
	AK3						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
	AL3						Yes No
	AM3						Yes No
	AN3						Yes No
	AO3						Yes No
	AP3						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DH	AR	3	Fu, D. et al., "A Digital Background Calibration Technique for Time-Interleaved Analog-to-Digital Converters," <i>IEEE Journal of Solid-State Circuits</i> , IEEE, Vol. 33, No. 12, December 1998, pages 1904-1911.
DH	AS	3	Guizani, M. and Al-Ali, A., "PC-Compatible Optical Data Acquisition Unit," <i>Instrumentation and Measurement Technology Conference</i> , IEEE, May 10-12, 1994, pages 1099-1102.
DH	AT	3	Jenq, Y-C., "Digital Spectra of Nonuniformity Sampled Signals: A Robust Sampling Time Offset Estimation Algorithm for Ultra High-Speed Waveform Digitizers Using Interleaving," <i>Transactions on Instrumentation and Measurement</i> , IEEE, Vol. 39, No. 1, February 1990, pages 71-75.

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U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA4						
	AB4						
	AC4						
	AD4						
	AE4						
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FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL4						Yes No
	AM4						Yes No
	AN4						Yes No
	AO4						Yes No
	AP4						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)			
DH	AR	4	Mason, R. and Taylor, J.T., "High-Speed Electro-Optic Analogue to Digital Converters," <i>IEEE International Symposium on Circuits and Systems</i> , IEEE, 1993, pages 1081-1084.
DH	AS	4	Niewczas, P. et al., "Error Analysis of an Optical Current Transducer Operating with a Digital Signal Processing System," <i>IEEE Transactions on Instrumentation and Measurement</i> , IEEE, Vol. 49, No. 6, December 2000, pages 1254-1259.
DH	AT	4	Petraglia, A. and Mitra, S., "Analysis of Mismatch Effects Among A/D Converters in a Time-Interleaved Waveform Digitizer," <i>IEEE Transactions on Instrumentation and Measurement</i> , IEEE, Vol. 40, No. 5, October 1991, pages 831-835.

EXAMINER	Duc Ho	DATE CONSIDERED	12-20-05
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FIRST SUPPLEMENTAL INFORMATION
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA5						
	AB5						
	AC5						
	AD5						
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	AJ5						
	AK5						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
	AL5						Yes No
	AM5						Yes No
	AN5						Yes No
	AO5						Yes No
	AP5						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DM	AR	5	Sauer-Greff, W. et al., "Maximum-Likelihood Sequence Estimation of Nonlinear Channels in High-Speed Optical Fiber Systems," April 6, 2001. Retrieved from the Internet: <URL: http://www.ftw.at/Dokumente/010406a.pdf , 29 pages.
DM	AS	5	Williamson, R.C. et al., "Effects of Crosstalk in Demultiplexers for Photonic Analog-to-Digital Converters," <i>Journal of Lightwave Technology</i> , IEEE, Vol. 19, No. 2, February 2001, pages 230-236.
DM	AT	5	Yang, C-K., "Design Techniques for High-Speed Chip-to-Chip Links," Retrieved from the Internet: <URL: http://web.doe.carleton.ca/courses/97578/topic5/Tutorial_SerialLink.pdf , 31 pages.

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	APPLICANT Gopinathan et al.	
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA6						
	AB6						
	AC6						
	AD6						
	AE6						
	AF6						
	AG6						
	AH6						
	AI6						
	AJ6						
	AK6						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
	AL6						Yes No
	AM6						Yes No
	AN6						Yes No
	AO6						Yes No
	AP6						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DA	AR	6	Yang, C-K. et al., "A Serial-Link Transceiver Based on 8-Gsamples/s A/D and D/A Converters in 0.25- μ m CMOS," <i>IEEE Journal of Solid-State Circuits</i> , IEEE, Vol. 36, No. 11, November 2001, pages 1684-1692.
DA	AS	6	Zuoxi, T., "Implementation of a Digital Multibeam Receiver Based on TMS320C80 for Laser Optoacoustic Remote Sensing," <i>Proceedings of ICSP2000</i> , IEEE, 2000, pages 2082-2084.
	AT	6	

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